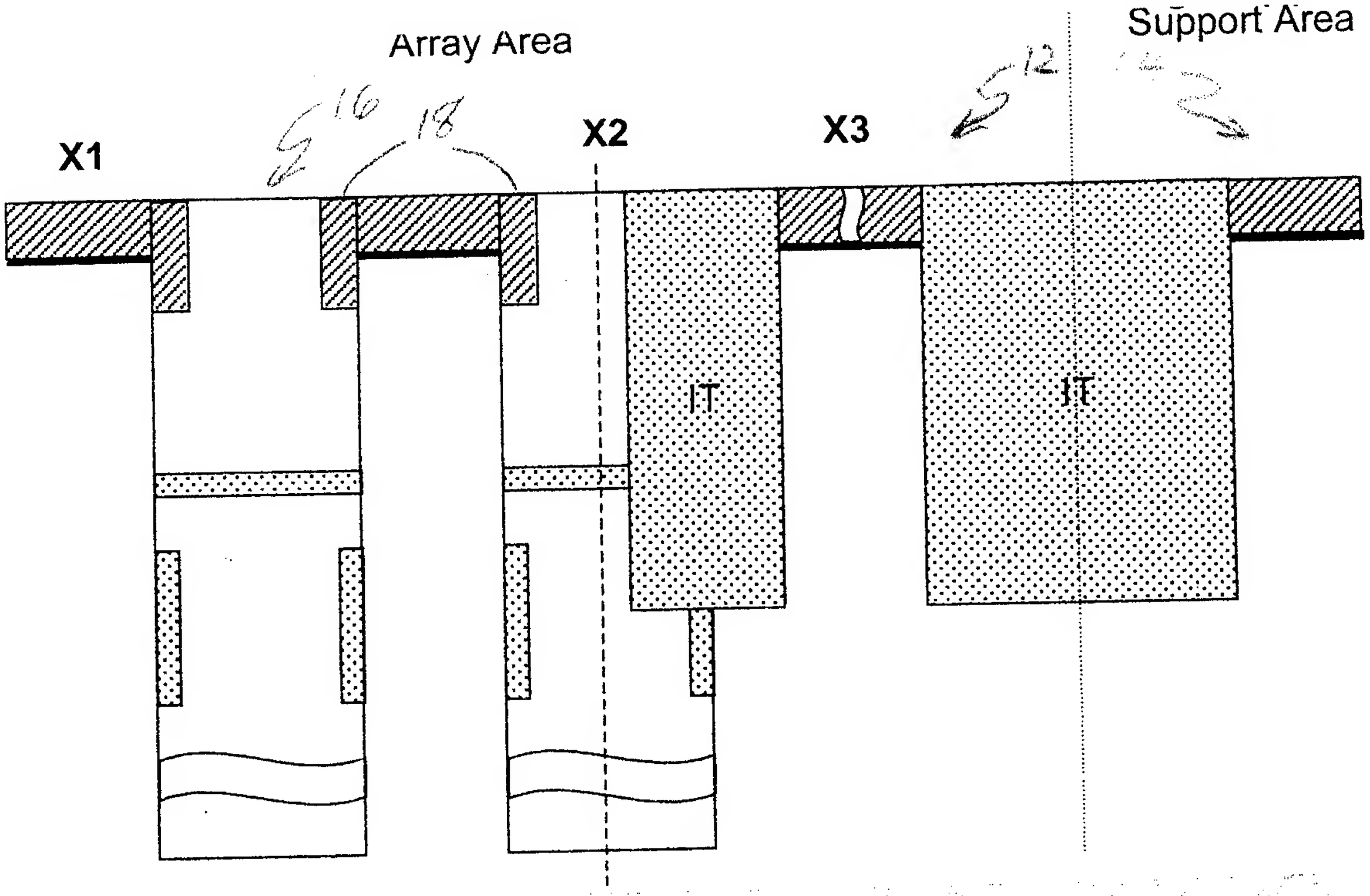
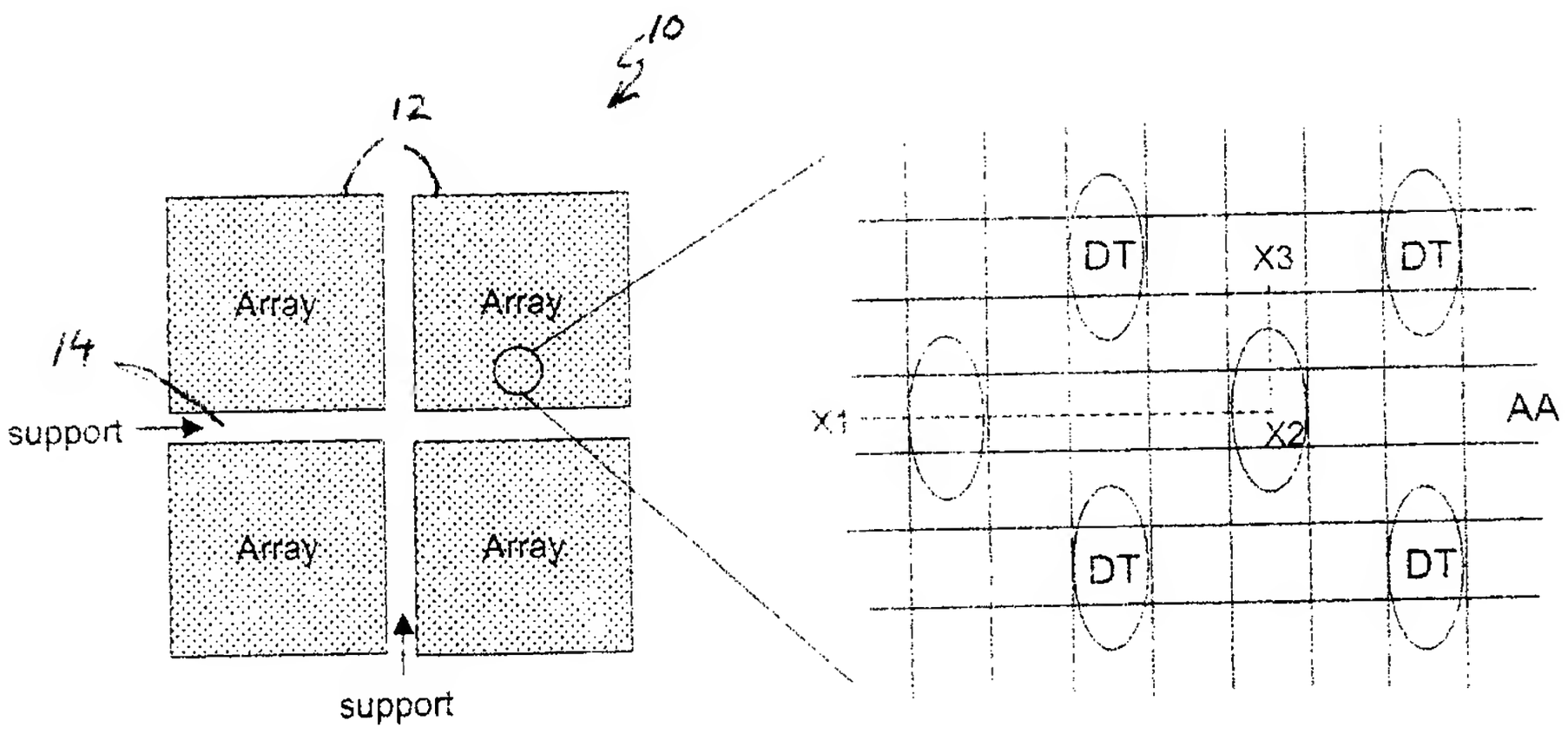

Array Area
Support Area
X1
X2
X3
16
18
12
14
IT
IT

Figure 1


10
12
Array
Array
Array
Array
14
support
support
DT
X3
DT
X1
X2
AA
DT
DT

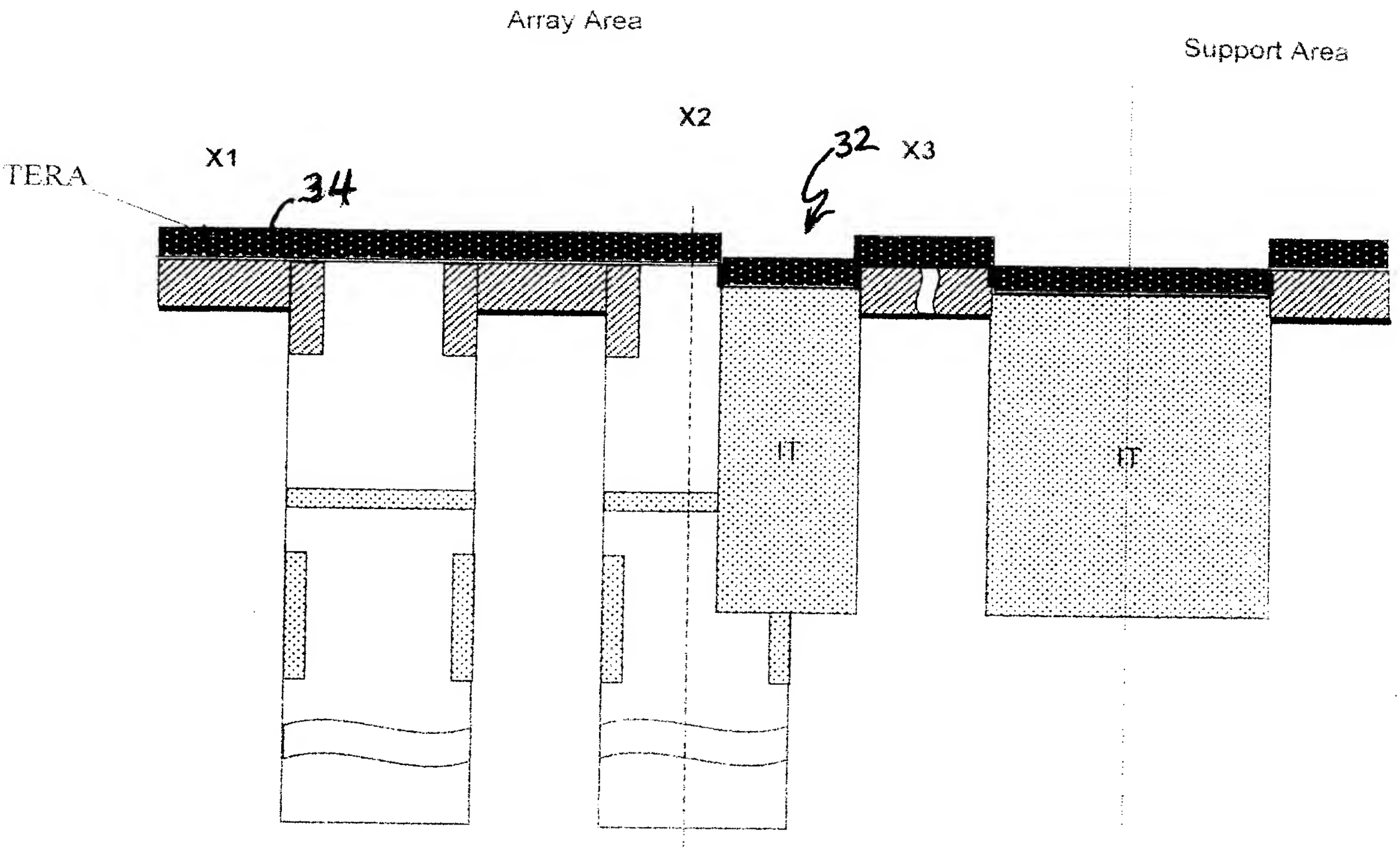


Figure 3

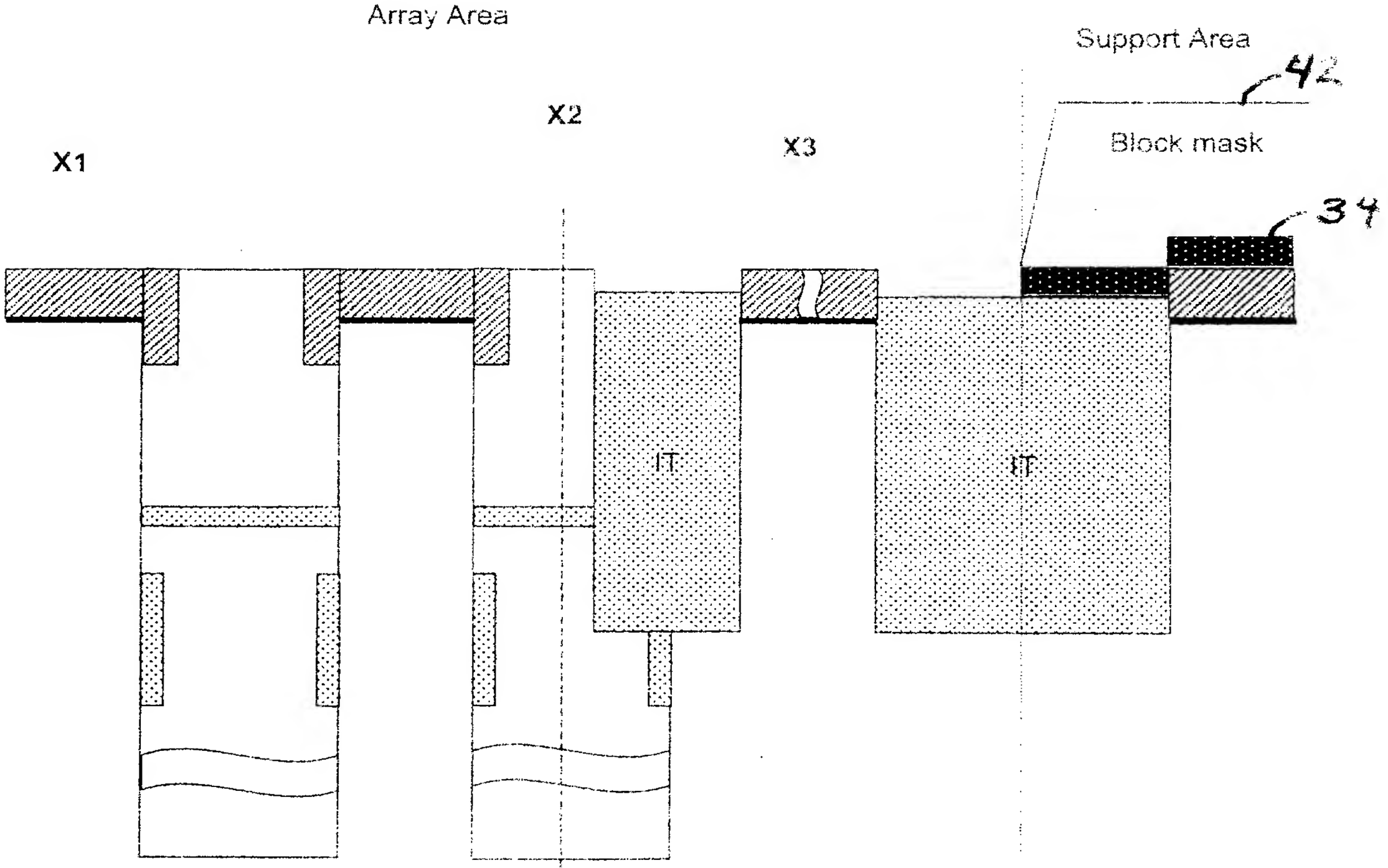
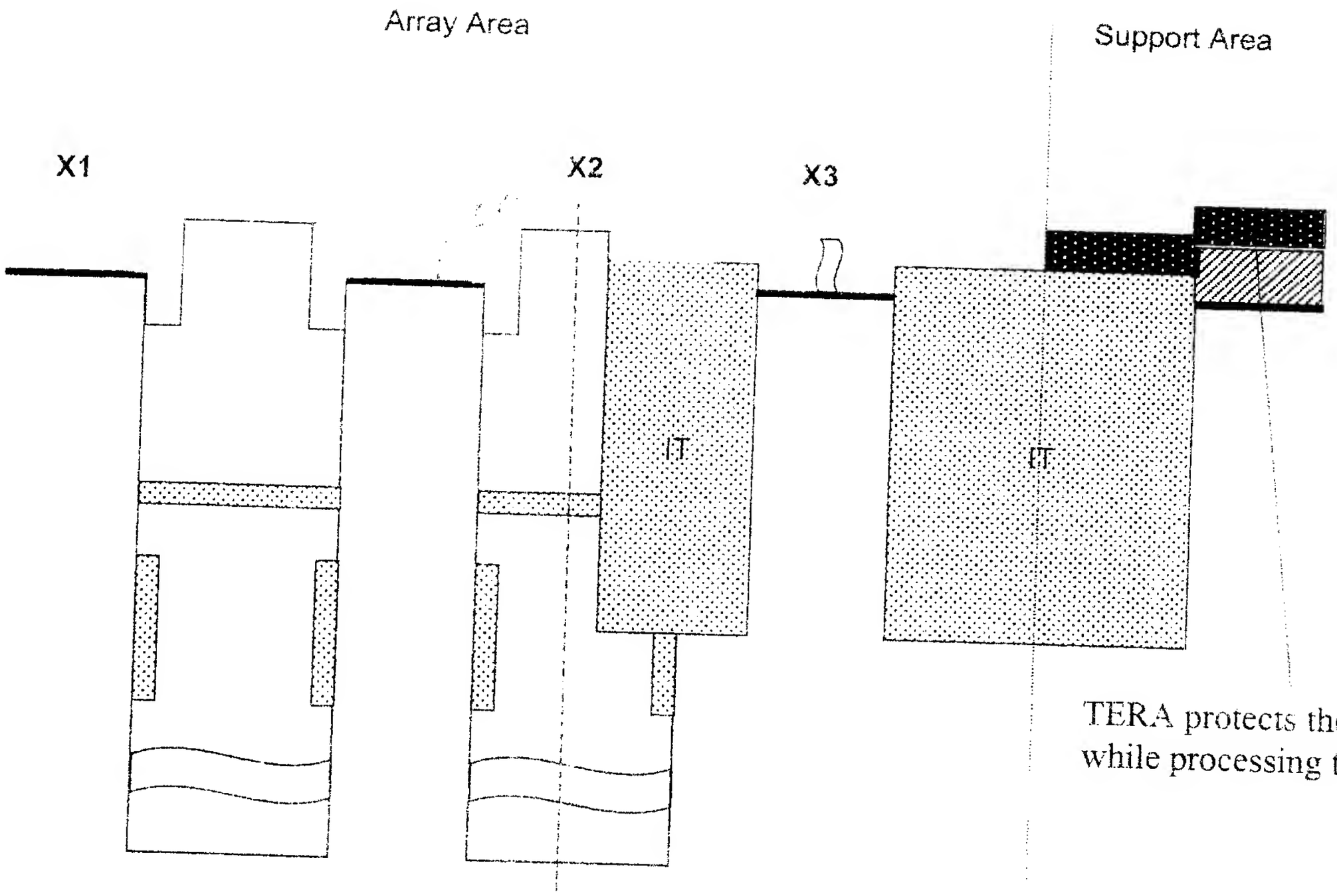


Figure 4



TERA protects the support area while processing the array area

Figure 5

Figure 6

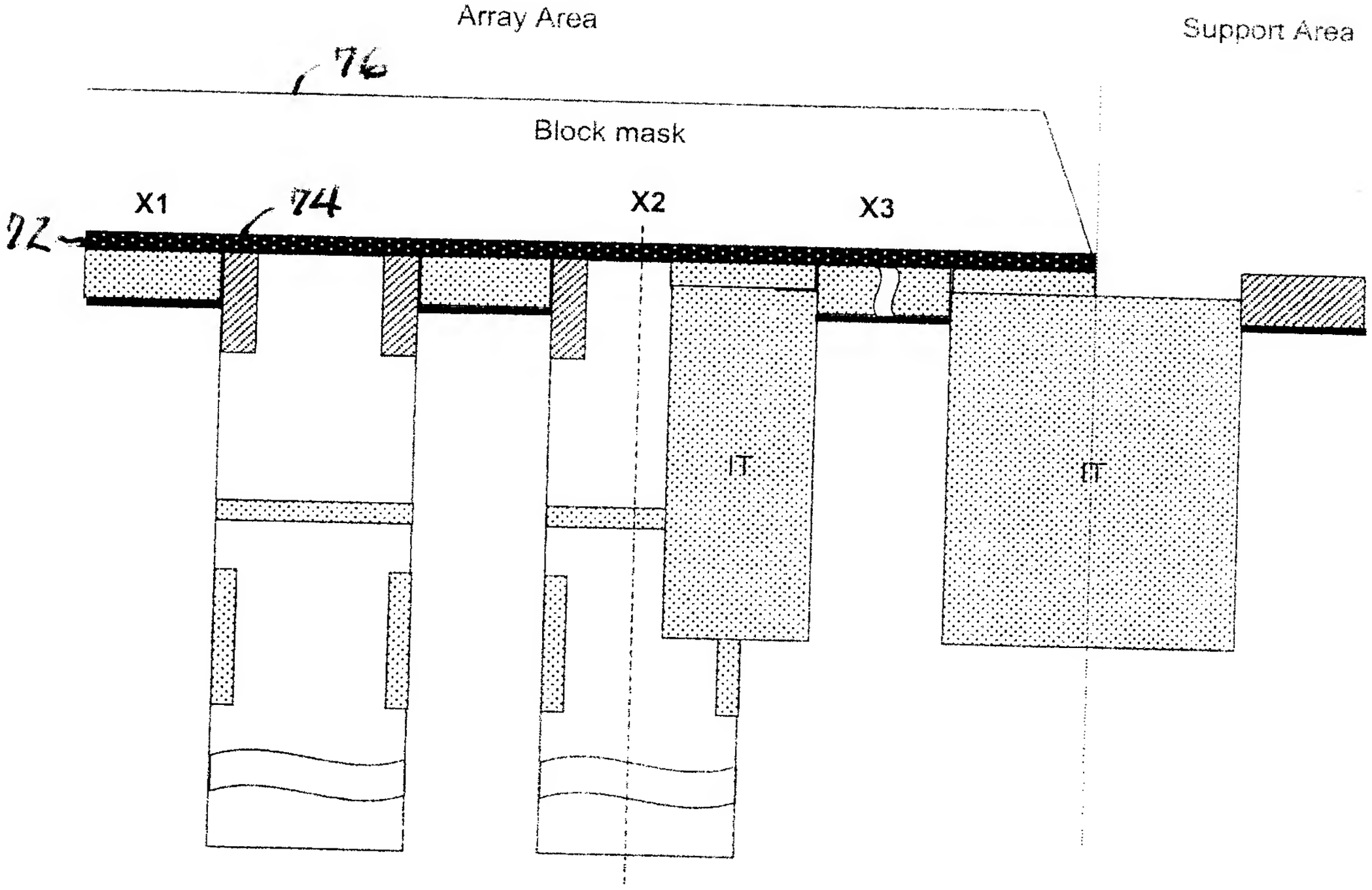


Figure 7

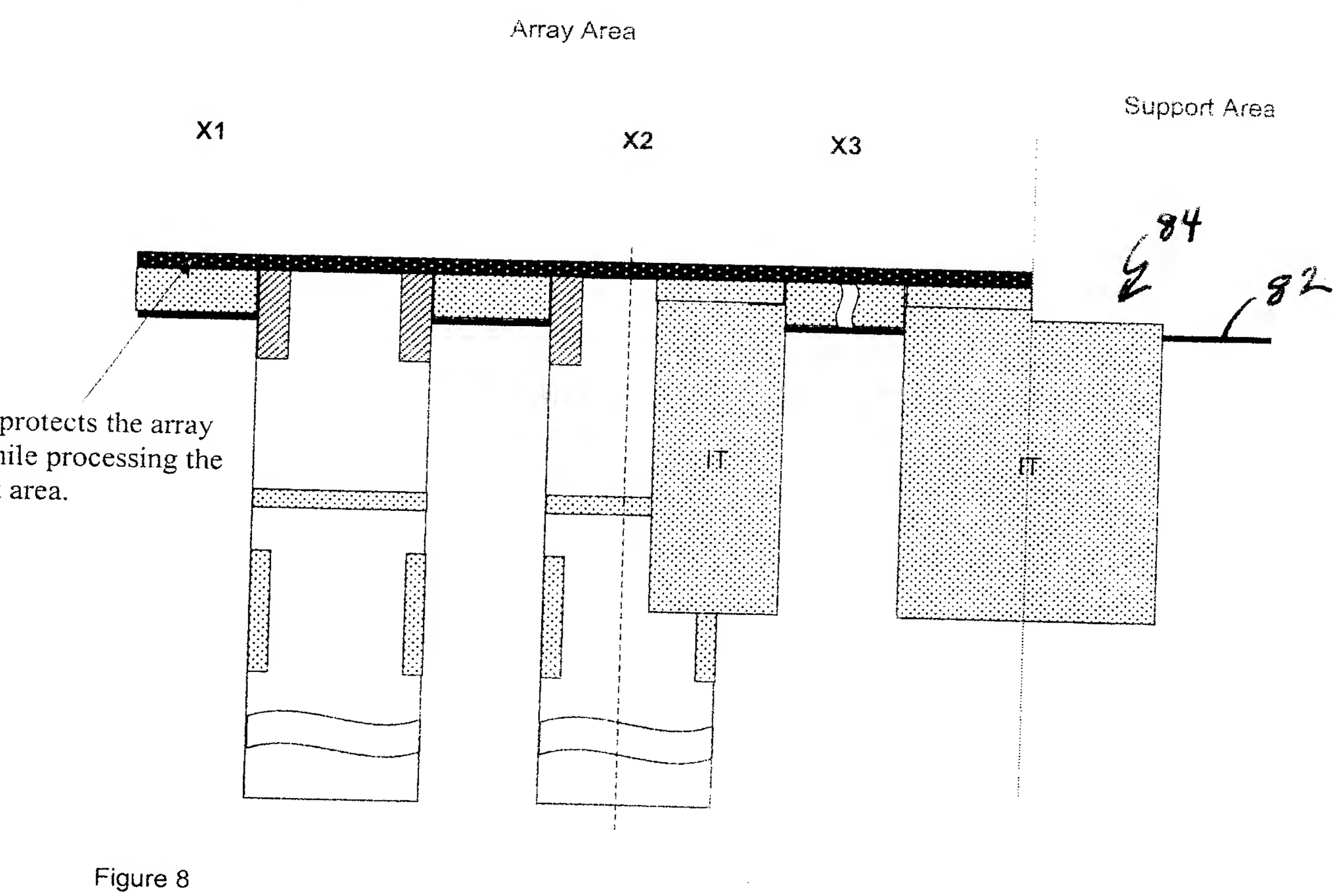


Figure 8

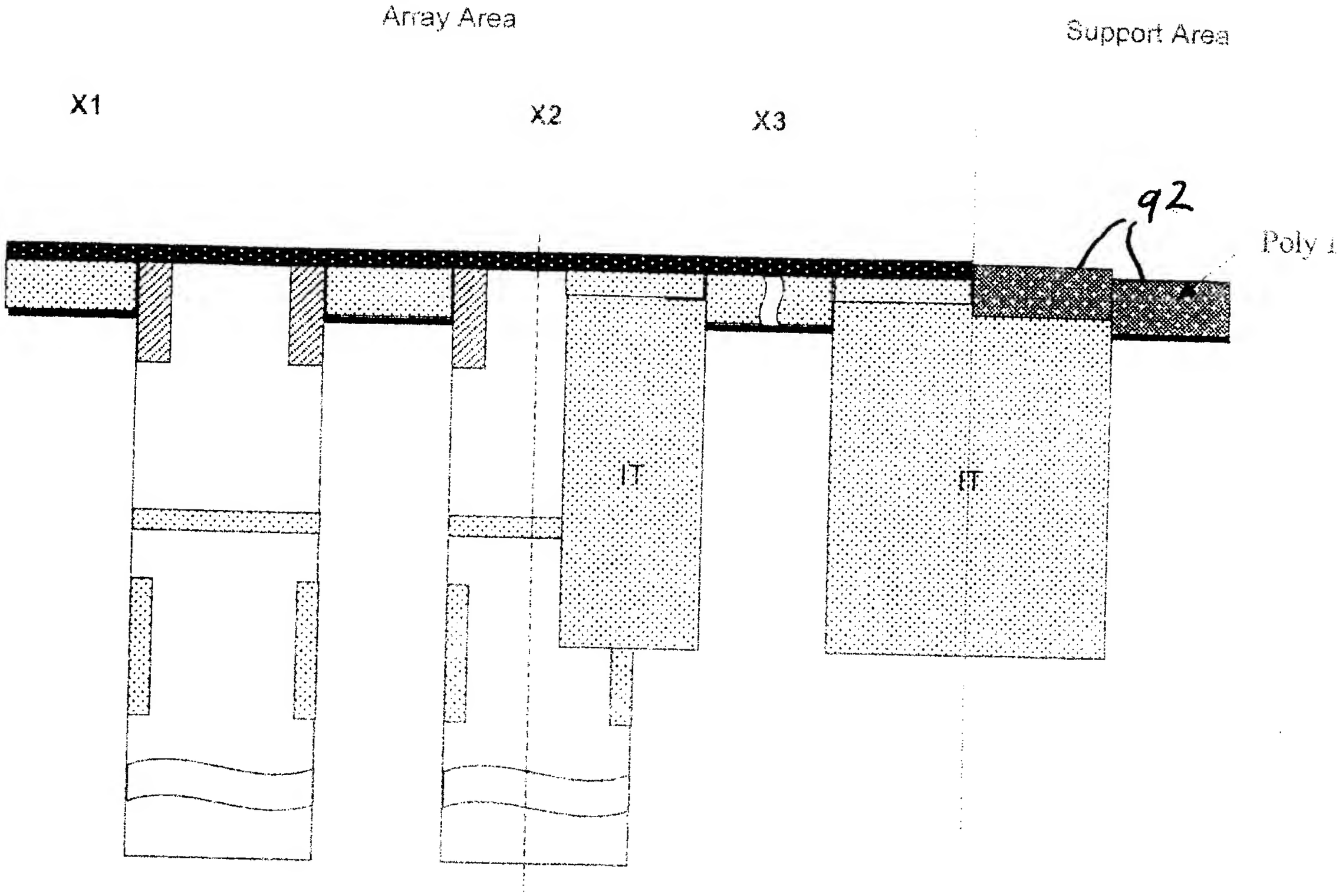


Figure 9

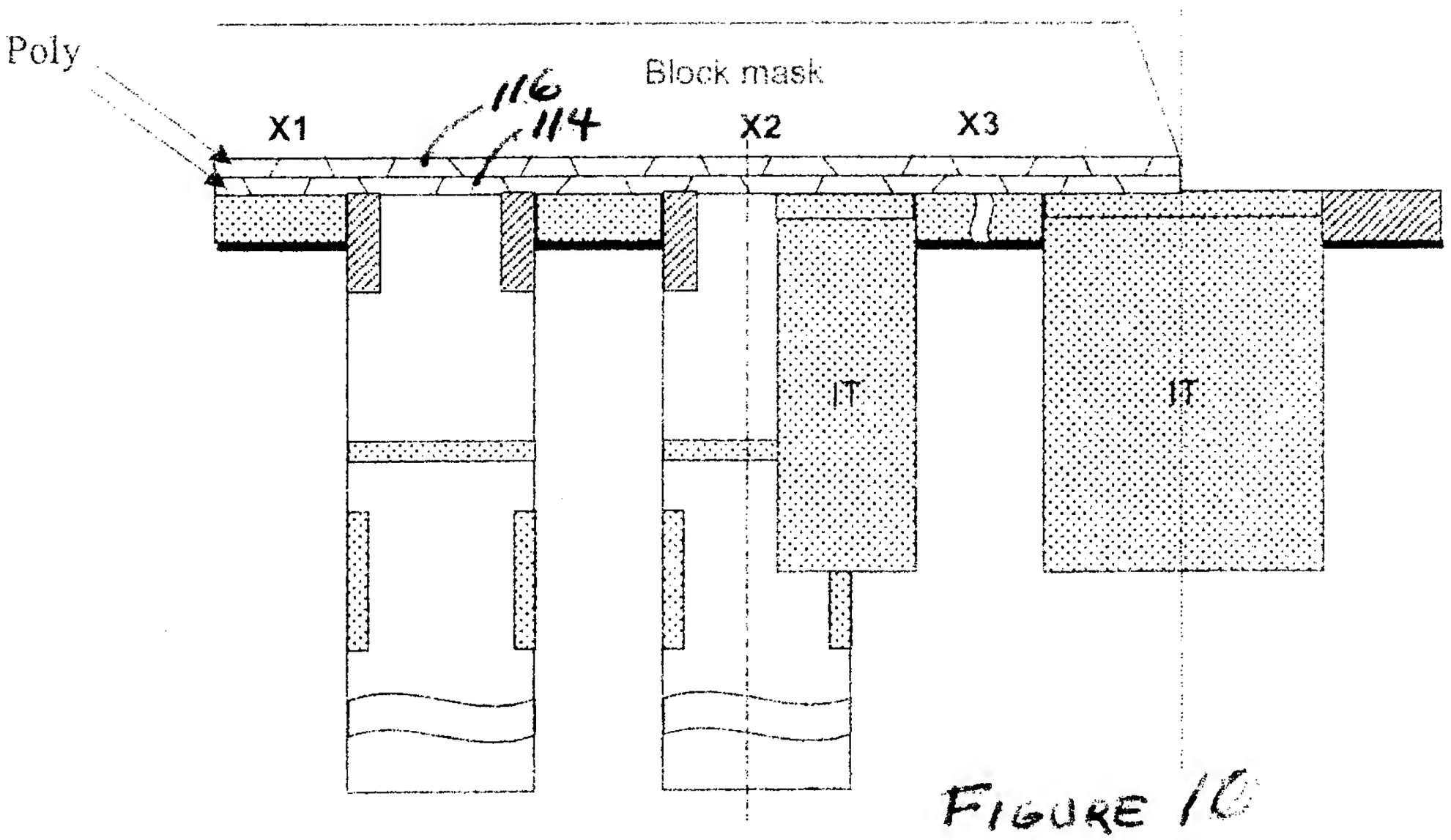


FIGURE 10

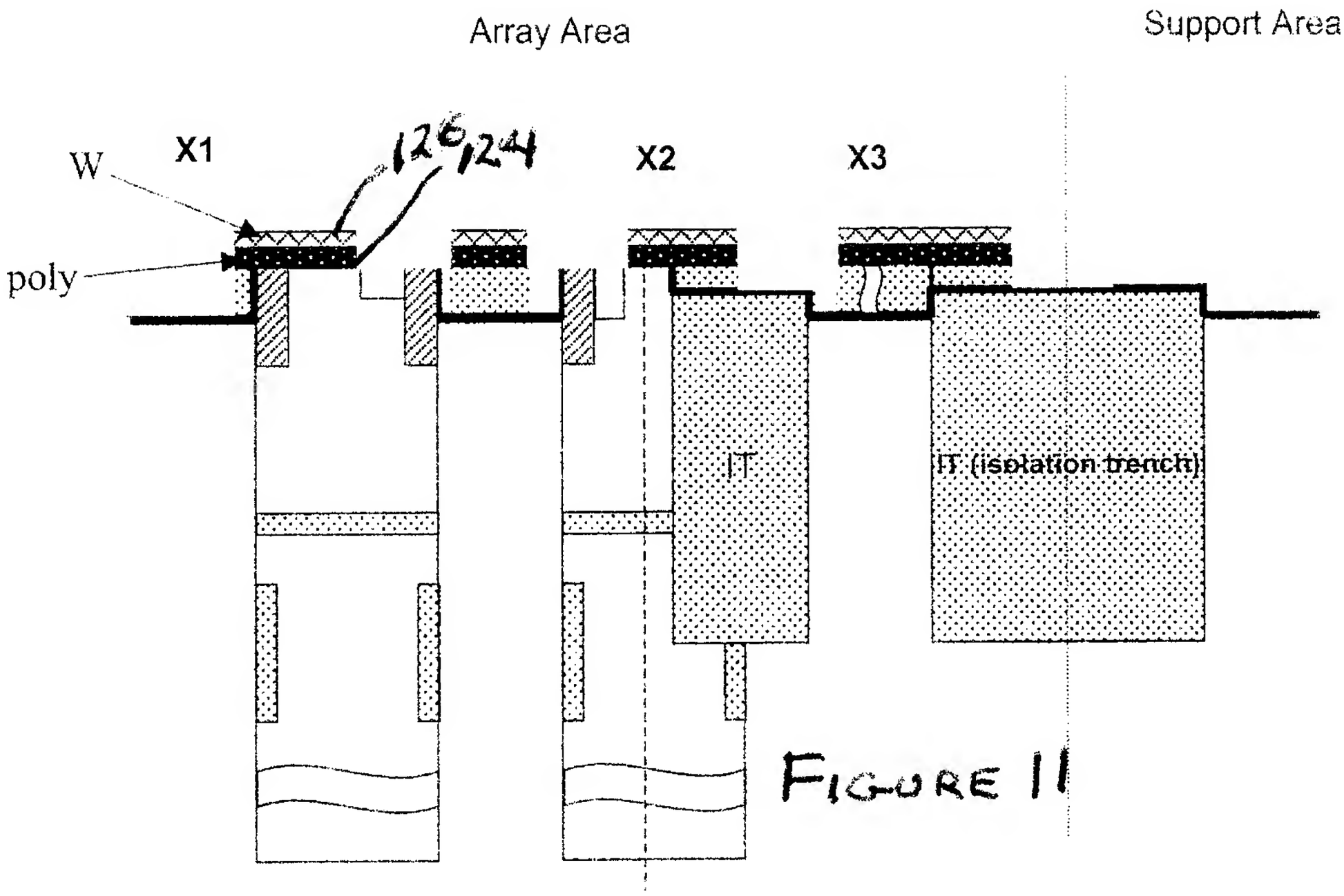


FIGURE 11

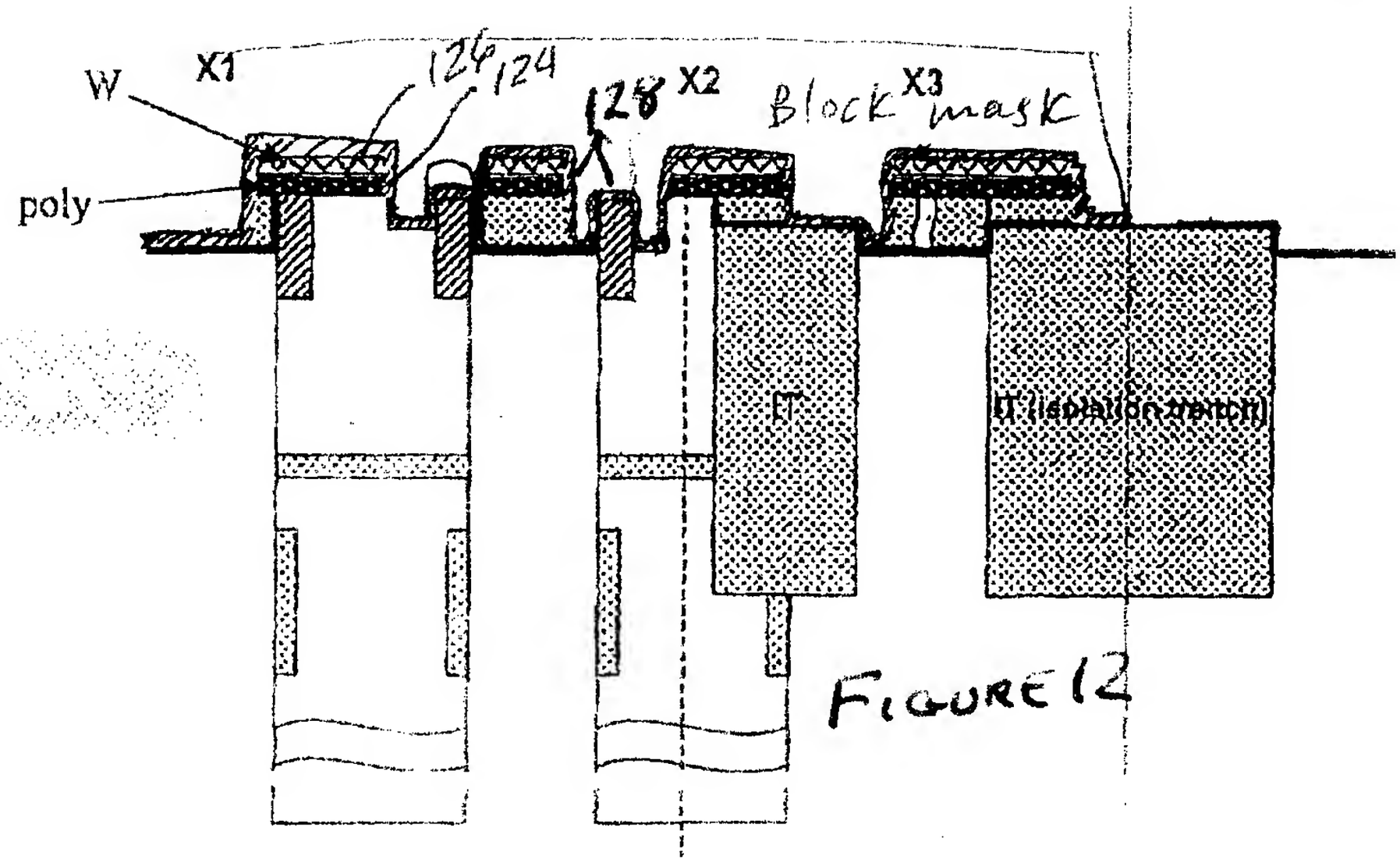


FIGURE 12